

PRIME has been established over recent years as an important conference where PhD students and post-docs with less than one year post-PhD experience can present their research results and network with experts from industry, academia and research. PRIME 2023 will feature a conference program reflecting the wide spectrum of research topics in Microelectronics and Electronics, building bridges between various research fields. In addition to the technical sessions, opportunities for the conference attendees will be the keynote talks, exhibits and social events. PRIME 2023 is seeking original papers in the areas given below. PRIME 2023 is technically co-sponsored by IEEE CAS Society. All accepted papers will be published in IEEE Xplore.

#### **Areas of Interest**

- Micro/Nanoelectronics
- Semiconductors
- Analog/Digital Signal Processing
- Computer Aided Design
- Analog/Digital/Mixed/RF IC Design
- Integrated Power ICs
- Sensors/Systems and MEMS
- RF, Microwave and mm-wave Circuits
- Semiconductor Memories
- VLSI and SoC Applications
- Visual Signal Processing
- Energy Harvesting
- Automotive
- Flexible Electronics
- •Technical Trends & Challenges

# rts. PRIME 2023 is Elisabetta Moisello, *Uni*

Elisabetta Moisello, *University of Pavia, Italy* Bernd Deutschmann, *TU Graz, Austria* 

Franco Maloberti, University of Pavia, Italy

Amara Amara Terre des hommes, France

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#### Venue

PRIME 2023 will be held in presence at Hotel Primus in Valencia, Spain.

### Company Fair

A Company Fair will be held at the conference venue to promote networking and cooperations with industries.

### **Best Paper Awards**

Gold Leaf Certificate: top 10%

Silver Leaf Certificate: between top 10% and top 20% Bronze Leaf Certificate: between top 20% and top 30%

### **Important Dates**

Submission deadline: 26<sup>th</sup> February 2023 Author notification: 1<sup>st</sup> April 2023 Final paper submission deadline: 29<sup>th</sup> April 2023

## **Steering Committee**

Franco Maloberti, *University of Pavia, Italy* Catherine Dehollain, *EPFL, Switzerland* Alberto Gola, *Power Integrations, Switzerland* Bernd Deutschmann, *TU Graz, Austria* Elena Blokhina, *Univ. College Dublin, Ireland* Gunhan Dundar, *Bogazici University, Turkey* Anton Klotz, *Cadence, Germany* Ravinder Dahiya, *University of Glasgow, UK* Nuno Horta, *University of Lisbon, Portugal* Edoardo Bonizzoni, *University of Pavia, Italy* 

